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SERIAL NUMBER 10/632,651	FILING DATE 07/31/2003 RULE	CLASS 327	GROUP ART UNIT 2816	ATTORNEY DOCKET NO. 020556
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APPLICANTS

Octavian Florescu, Kanata, CANADA;

** CONTINUING DATA *no*

** FOREIGN APPLICATIONS *no*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 10/28/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature _____ Initials <i>LF</i>	STATE OR COUNTRY CANADA	SHEETS DRAWING 6	TOTAL CLAIMS 33	INDEPENDENT CLAIMS 5
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ADDRESS
 23696
 Qualcomm Incorporated
 Patents Department
 5775 Morehouse Drive
 San Diego, CA
 92121-1714

TITLE
 Delay matching for clock distribution in a logic circuit

FILING FEE RECEIVED 1782	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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